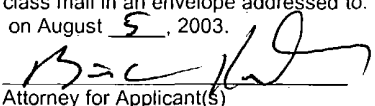


CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 5, 2003.


Attorney for Applicant(s)

PATENT APPLICATION

Docket No.: SJO920020151US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Allen King et al.)
Serial No.: Not yet assigned)
Filing Date: August 5, 2003) Group Art
For: APPARATUS, SYSTEM, AND METHOD FOR) Unit:
ARRAYING ELECTRICAL DEVICES IN A CABINET)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Information Disclosure Statement discloses information which has come to the attention of applicant and his attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), this Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicant nor his attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or as an admission that the

information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, this Information Disclosure Statement includes and is accompanied by:

1. A completed copy of Form PTO-1449 listing the patents, publications and other information being submitted for consideration; and
2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Form PTO-1449.

NON-ENGLISH INFORMATION

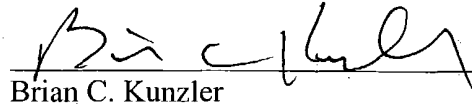
Pursuant to 37 C.F.R. § 1.98, following is a concise explanation of the relevance (as it is presently understood by the individual designated in 37 C.F.R. § 1.56(c) most knowledgeable about the content of the information), of each listed patent, publication or other information that is not in the English language.

JP 07-219717: DISK ARRAY DEVICE

PURPOSE: To miniaturize a disk array device and a computer device by mounting the disk array control means and plural disk devices on a substrate connected to the mother board of a computer.

CONSTITUTION: A mother board connection connector 104, an extension board 101, the disk array control circuit 102, a bus control circuit 103, the disk devices 105a-105b, disk device connectors 106a-106b and LED 107a-107d are provided. The extension board 101 is connected to the mother board of the computer by the mother board connection connector 104. The disk device connectors 106a-106d are ones for connecting the disk devices 105a-105d to the extension board 101. The disk device connectors 106a-106d are mounted so that they can attach/detach the disk devices 105-105d in parallel to the attaching/detaching directions of the extension board 101. There are LEDs 107 as many as the number of the disk devices 105 and they display the disk device in failure.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Brian C. Kunzler", written over a horizontal line.

Brian C. Kunzler
Reg. No. 38,527
Attorney for Applicant

Date: August 5, 2003

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FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	SERIAL NO. Not yet assigned	ATTORNEY DOCKET NO. SJO920020151US1
	FILING DATE August 5, 2003	GROUP ART UNIT
	APPLICANT(S): Allen King et al.	

REFERENCE DESIGNATION**U.S. PATENT DOCUMENTS**

EXAMINE R INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,136,466	08/04/1992	Remise et al.	361/391	10/05/1990
	A2	5,506,750	04/09/1996	Carteau et al.	361/685	04/22/1994
	A3	5,566,049	10/15/1996	Nguyen	361/685	07/07/1995
	A4	5,731,951	03/24/1998	Michaud et al.	361/685	09/06/1996
	A5	6,052,278	04/18/2000	Tanzer et al.	361/685	11/13/1998
	A6	6,112,276	08/29/2000	Hunt et al.	711/112	10/10/1997
	A7	6,392,875	05/21/2002	Erickson et al.	361/683	12/28/2000
	A8	6,400,578	06/04/2002	Chen	361/829	10/06/2000
	A9	6,445,578	09/03/2002	Bell et al.	361/685	08/30/2000
	A10	6,459,571	10/01/2002	Carteau	361/684	06/15/2000

FOREIGN PATENT DOCUMENTS

EXAMINE R INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION	
						YES	NO
	A11	JP 07-219717	Aug. 18, 1995	Japan	G06F 3/06		

NON-PATENT DOCUMENTS

EXAMINE R INITIAL		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).